

3976 U.S. PTO  
10/021746

TC119



U.S. UTILITY Patent Application

PATENT NUMBER and  
ISSUE DATE

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10021746	12/11/2001	324		2658	

**APPLICANTS:** Eichin Matthias, Kurz Alexander

**CONTINUING DATA VERIFIED:**

**FOREIGN APPLICATIONS VERIFIED:**  
GERMANY 100 64 478.3 12/22/2000 *Ver. 1.1*

PG-F11? ☐ DO NOT PUBLISH ☐ RESCIND ☐

Priority claimed ☐ yes ☒ no  
35 USC 112 conditions met ☐ yes ☒ no

Attorney Docket No. 4284

TITLE: Method for testing an integrated circuit

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>CLAIMS ALLOWED</b>	
		Total Claims	Print Claim for O.G.
<b>ISSUE FEE</b>		<b>DRAWING</b>	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg. Print Fig.
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		<b>Application Examiner</b>	
		<b>PREPARED FOR ISSUE</b>	
<b>WARNING:</b> The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.			

FILED WITH:

☐ DISK (CRF)

☐ CD-ROM

(Attached in pocket on right inside flap)